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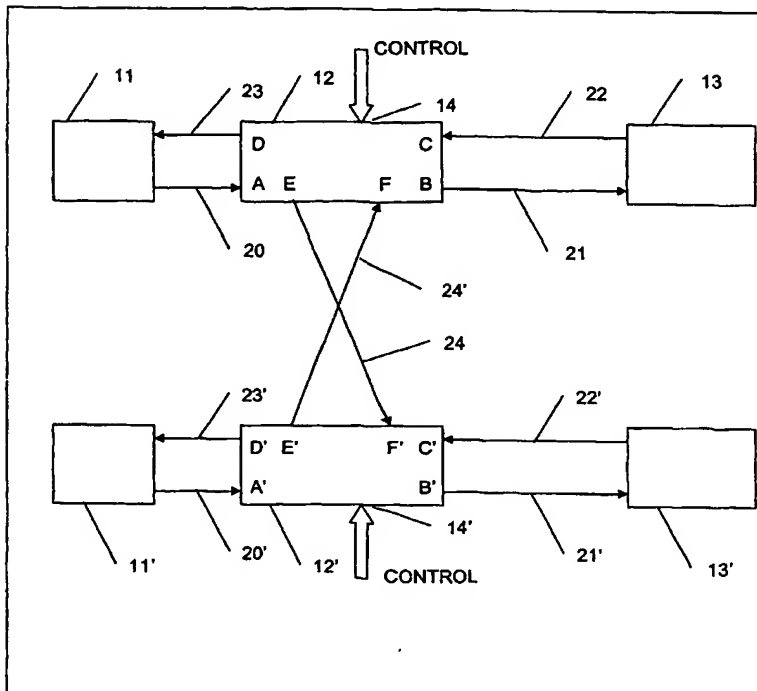
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(54) Title: CONNECTION APPARATUS AND METHOD FOR NETWORK TESTERS AND ANALYSERS



(57) Abstract: Connection apparatus (10) for a network tester or analyser comprises at least two network connection devices (11,11') for connecting the apparatus to a network and at least two solid state switches (12,12'). Each connection device (11,11') is constructed and arranged to output serial electrical signals (20,20') corresponding to signals received from a network to which the connection apparatus (10) is in use connected. Each solid state switch (12,12') is constructed and arranged to receive serial electrical signals (20,20') output by a respective one of the network connection devices (11,11') and to output a corresponding serial electrical signal (21,24,21',24'). Each solid state switch (12,12') is controllable such that electrical signals (24,24') corresponding to signals received from the network can selectively be output by the solid state switch (12,12') and received at the other or another of the solid state switches (12',12) for return to the network by said other or another of the solid state switches (12',12).

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CONNECTION APPARATUS AND METHOD
FOR NETWORK TESTERS AND ANALYSERS

The present invention relates to connection apparatus
5 and methods for network testers and analysers.

Network testers are commonly used to test certain
elements of a network. For example, a network tester may
test the integrity of physical aspects of the network, such
10 as the cables (such as electric wires or fibre optics),
and/or logical aspects. This may be done by the network
tester generating (dummy) network traffic that is passed to
the network and then subsequently analysed. On the other
hand, a network analyser, also known as a protocol analyser
15 or a network monitor, analyses data passing along the
network, typically by capturing and/or copying data packets
from the network and carrying out various analyses on the
data packets.

20 Network analysers are often connected in in-line mode.
In other words, the network analyser is effectively
connected between two network devices, capturing data
packets passing between the two network devices whilst not
affecting the passage of data packets between the network
25 devices. Where the network is an electrical network, this
may require that the network analyser provide copies of the
captured data packets back to the network. Where the
network is an optical network, the connection arrangement
for the network analyser typically only splits off a
30 portion of the light such that it is not necessary for
captured data packets to be copied back to the network by
the analyser. Network testers tend in practice to be
connected only in end station mode. Nevertheless, in the

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case of both network testers and network analysers, it can be convenient to be able to selectively connect the tester or analyser such that it operates in in-line mode or end station mode at the option of the user.

5

Many network testers and analysers are constructed so that they can operate only in in-line or in end station mode, and the user is unable to change the configuration of the network tester or analyser to operate in the other
10 mode. One known way of allowing a network tester or analyser to be switched between end station or in-line mode at the option of the operator is to use mechanical switches or relays within the tester or analyser to switch the signal paths appropriately. However, mechanical switches
15 or relays are preferably avoided in network analysers or testers that are used with high transmission rate networks because they can leave undesirable artefacts on the signals passing to and/or from the network tester or analyser.

20 According to a first aspect of the present invention, there is provided connection apparatus for a network tester or analyser, the connection apparatus comprising: at least two network connection devices for connecting the apparatus to a network, each connection device being constructed and
25 arranged to output serial electrical signals corresponding to signals received from a network to which the connection apparatus is in use connected; and, at least two solid state switches, each solid state switch being constructed and arranged to receive serial electrical signals output by
30 a respective one of the network connection devices and to output a corresponding serial electrical signal; each solid state switch being controllable such that electrical signals corresponding to signals received from a said

network can selectively be output by the solid state switch and received at the other or another of the solid state switches for return to a said network by said other or another of the solid state switches.

5

The use of controllable solid state switches provides a simple and flexible connection topology. The connection apparatus can be connected to or formed as part of the "front end" a network tester or analyser so that the tester or analyser can be connected to a network to operate in in-line or end station mode at the option of the operator. Because the switches deal with electrical signals at the serial level, minimal or practically negligible latency is introduced into the electrical signals as they are handled by the switches. This is particularly important in high speed networks, such as those operating at rates of gigabits per second or higher. The connection apparatus can be embodied on a single printed circuit board, which may be part of a network tester or analyser.

20

The connection apparatus may comprise a respective serial-to-parallel data converter for each solid state switch, each serial-to-parallel data converter being constructed and arranged to receive a serial electrical signal corresponding to signals received from a said network that is output by the respective solid state switch and to convert the received serial electrical signal into parallel form. The output parallel signals can be passed further into a network tester or analyser for testing/analysis purposes in a manner known per se.

30

Each solid state switch is preferably constructed and arranged to retime electrical signals received from the

other or another of the solid state switches prior to returning said electrical signals to a said network. The use of retiming is particularly advantageous when operating in in-line mode as it ensures that the integrity of the data returned to the network is preserved. This is valuable in any network protocol but is particularly useful where the network uses the Fibre Channel standard as it can help to avoid the need for fill words to be added to or removed from the data that is returned to the network. It will be understood that the need to add or remove fill words adds to the complexity and therefore cost of a network analyser. Avoiding this also means that the network analyser interferes with the data as little as possible. Retiming at the serial level also avoids the process of de-serialisation, decoding, skew management, and re-serialisation. In other words, when retiming at the serial level, the solid state switch effectively retimes the signal to itself by locking onto the incoming serial data and generating a periodic clock signal which is then used to derive the transmitted data. This reduces the unwanted effects of transmission across optical or copper medium which would be associated with for example simply buffering the input signal. In Fibre Channel when retiming is done at a higher level, i.e. after de-serialisation and decoding, etc., then so-called skew management (i.e. the use of addition or removal of fill words) is required because the signal is retimed to a new reference clock.

Preferably, at least one of the solid state switches is a port bypass circuit. In a preferred embodiment, each solid state switch is a port bypass circuit. Port bypass circuits, which are known per se for connecting network devices, are typically well adapted for use with high speed

networks. The preferred port bypass circuits provide the retiming function discussed above.

Each network connection device may be constructed and
5 arranged to receive optical signals from an optical network and to convert the received optical signals into serial electrical form for output to the respective solid state switch.

10 According to a second aspect of the present invention, there is provided connection apparatus for network testers and analysers, the connection apparatus comprising: two network connection devices for connecting the apparatus to a network, each connection device being constructed and
15 arranged to output serial electrical signals corresponding to signals received from a network to which the connection apparatus is in use connected; and, two port bypass circuits, each port bypass circuit having at least three output ports, each port bypass circuit being constructed
20 and arranged to receive serial electrical signals output by a respective one of the network connection devices and to output a corresponding serial electrical signal on a first of its output ports; each port bypass circuit being
25 controllable such that electrical signals corresponding to signals received from a said network can selectively be output on a second of the output ports of the port bypass circuit and received at the other port bypass circuit for return to a said network via a third of the output ports of the other port bypass circuit.

30

As mentioned above, port bypass circuits are typically well adapted for use with high speed networks.

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The connection apparatus may comprise a respective serial-to-parallel data converter for each port bypass circuit, each serial-to-parallel data converter being constructed and arranged to receive a serial electrical
5 signal corresponding to signals received from a said network that is output on the first port of the respective port bypass circuit and to convert the received serial electrical signal into parallel form.

10 Each port bypass circuit is preferably constructed and arranged to retime electrical signals received from the other or another of the port bypass circuits prior to returning said electrical signals to a said network.

15 According to another aspect of the present invention, there is provided a network tester comprising connection apparatus as described above so that the network tester can selectively be operated in in-line or end station mode when connected to a network.

20 According to another aspect of the present invention, there is provided a network analyser comprising connection apparatus as described above so that the network analyser can selectively be operated in in-line or end station mode
25 when connected to a network.

According to another aspect of the present invention, there is provided a method of operating connection apparatus for a network tester or analyser, the connection
30 apparatus comprising at least two network connection devices for connecting the apparatus to a network, each connection device being constructed and arranged to output serial electrical signals corresponding to signals received

from the network; and, at least two solid state switches, each solid state switch being constructed and arranged to receive serial electrical signals output by a respective one of the network connection devices and to output a
5 corresponding serial electrical signal; the method comprising: selectively controlling each solid state switch such that electrical signals corresponding to signals received from the network are output by the solid state switch and received at the other or another of the
10 solid state switches for return to the network by said other or another of the solid state switches whereby the apparatus operates in in-line mode, or such that electrical signals corresponding to signals received from the network and output by each solid state switch are not received at
15 the other or another of the solid state switches whereby the apparatus operates in end station mode.

According to another aspect of the present invention, there is provided a method of operating connection
20 apparatus for network testers and analysers, the connection apparatus comprising: two network connection devices for connecting the apparatus to a network, each connection device being constructed and arranged to output serial electrical signals corresponding to signals received from a
25 network to which the connection apparatus is in use connected; and, two port bypass circuits, each port bypass circuit having at least three output ports, each port bypass circuit being constructed and arranged to receive serial electrical signals output by a respective one of the
30 network connection devices and to output a corresponding serial electrical signal on a first of its output ports; the method comprising: selectively controlling each port bypass circuit such that electrical signals corresponding

to signals received from the network are output on a second of the output ports of the port bypass circuit and received at the other port bypass circuit for return to the network via a third of the output ports of the other port bypass circuit whereby the apparatus operates in in-line mode, or
5 such that electrical signals corresponding to signals received from the network and output by each port bypass circuit are not received at the other port bypass circuit whereby the apparatus operates in end station mode.

10

Embodiments of the present invention will now be described by way of example with reference to the accompanying drawing, in which:

15 Fig. 1 is a schematic block diagram of an example of connection apparatus according to an embodiment of the present invention.

Referring to the drawing, an example of connection
20 apparatus 10 in accordance with an embodiment of the present invention is used as a front end of a network tester or analyser (not shown). The connection apparatus 10 may be connected to or integrally formed with the network tester or analyser. The connection apparatus 10
25 has two network connection devices 11,11' which allow the connection apparatus 10 to receive signals from a network (not shown) to which the connection apparatus 10 is in use connected. The network connection devices 11,11' provide output serial electrical signals 20,20'. Where the network
30 is an optical network, each network connection device 11,11' may be such as to convert the received optical signals into the output electrical signals 20,20'.

The output electrical signals 20,20' are passed to respective solid state switches 12,12'. The solid state switches 12,12' are such as to be able to cope with the physical link rate of the network. As will be discussed
5 further below, each switch 12,12' of the preferred embodiment is a so-called port bypass circuit.

Each switch 12,12' of this example has a control input 14,14' on which control signals for controlling the
10 operation of the switch 12,12' can be presented. Each switch 12,12' of this example further has plural input and output ports which are arranged as follows.

Each switch 12,12' has a first input port A,A' at
15 which the electrical signals 20,20' from the respective network connection devices 11,11' are received. Each switch 12,12' outputs on a first output port B,B' a serial electrical signal 21,21' corresponding to the electrical signals 20,20' received at its input port A,A', the output
20 serial electrical signals 21,21' being passed to a respective SERDES 13,13'. In the preferred embodiment, the output serial electrical signals 21,21' are always sent to the respective SERDES 13,13' when the network tester or analyser is operating.

25

Each SERDES 13,13' provides for serial-to-parallel conversion of the received serial electrical signals 21,21', the parallel signals being passed to known components of the network tester or analyser.
30 Correspondingly, each SERDES 13,13' can receive parallel signals and convert them to serial form which is output as serial electrical signals 22,22' which are returned to a second input port C,C' of the respective switches 12,12'.

Each switch 12,12' has a second output port D,D' on which electrical signals 23,23' are output to the respective network connection device 11,11'. Each switch
5 12,12' has a third output port E,E' on which electrical signals 24,24' can be output to be received at a third input port F',F of the other switch 12',12.

The control signals presented at the control inputs
10 14,14' of the switches 12,12' cause the switches 12,12' to be configured so as to enable the connection apparatus 10 to be operated either in in-line mode or end station mode at the option of the operator. In particular, the control signals cause the various input and output ports A-F,A'-F'
15 to be connected as follows.

In in-line mode, the switches 12,12' are controlled so that the electrical signals 20 received at the first input port A are copied so as to be output on the third output
20 port E and passed as the electrical signals 24 to the third input port F' of the other switch 12' (and correspondingly for the electrical signals 20' received at the first input port A' of the other switch 12'). This is in addition to the passing of the electrical signal 21 from the first
25 output port B to the SERDES 13 (and correspondingly for the other switch 12'). The third input port F' of the other switch 12' passes the received electrical signal 24 to the second output port D' where it is passed as the output electrical signal 23' to the respective network connection
30 device 11' (and correspondingly for the other switch 12). In the preferred embodiment, the signal passing between the third input port F,F' to the second output port D,D' is regenerated by the switch 12,12' in order to restore signal

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amplitude and retimed to reduce jitter, thus improving signal integrity such that the ongoing signal passed back to the network is less degraded. It will be appreciated that this arrangement provides for a crossover path from one half of the connection apparatus 10 to the other, thus providing a duplex path in both directions.

On the other hand, when operating in end station mode, the third output ports E,E' of the switches 12,12' are not arranged to receive signals from the first input ports A,A' and so do not transmit signals to the other switch 12',12. On the contrary, the signals 23 that are passed to the network connection devices 11,11' correspond to the signals 22,22' received at the second input ports C,C'. It will be appreciated that in this end station mode, the two halves of the connection apparatus 10 can operate as independent end port stations, each capable of transmitting and receiving.

Accordingly, in a simple and effective manner, the connection apparatus 10 can be controlled by an operator so that the connection apparatus 10 can be used selectively in in-line or end station mode at the option of the operator.

In the preferred embodiment, as briefly mentioned above, the switches 12,12' are so-called port bypass circuits. Suitable port bypass circuits include the Max3755 from Maxim, the VSC7147 from Vitesse, and the HDMP-0552 from Agilent. Each of these provides optionally for regeneration of a received electrical signal to provide for clock and data recovery so that when the connection apparatus 10 is operating in in-line mode, the integrity of the signal returned to the network is maintained. Such

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port bypass circuits have conventionally been used only to interconnect network devices, such as disk drives, personal computers, etc.

- 5 Embodiments of the present invention have been described with particular reference to the examples illustrated. However, it will be appreciated that variations and modifications may be made to the examples described within the scope of the present invention.

CLAIMS

1. Connection apparatus for a network tester or analyser, the connection apparatus comprising:
 - 5 at least two network connection devices for connecting the apparatus to a network, each connection device being constructed and arranged to output serial electrical signals corresponding to signals received from a network to which the connection apparatus is in use connected; and,
 - 10 at least two solid state switches, each solid state switch being constructed and arranged to receive serial electrical signals output by a respective one of the network connection devices and to output a corresponding serial electrical signal;
 - 15 each solid state switch being controllable such that electrical signals corresponding to signals received from a said network can selectively be output by the solid state switch and received at the other or another of the solid state switches for return to a said network by said other
 - 20 or another of the solid state switches.
2. Connection apparatus according to claim 1, comprising a respective serial-to-parallel data converter for each solid state switch, each serial-to-parallel data converter
25 being constructed and arranged to receive a serial electrical signal corresponding to signals received from a said network that is output by the respective solid state switch and to convert the received serial electrical signal into parallel form.
- 30 3. Connection apparatus according to claim 1 or claim 2, wherein each solid state switch is constructed and arranged to retime electrical signals received from the other or

another of the solid state switches prior to returning said electrical signals to a said network.

4. Connection apparatus according to any of claims 1 to
5 3, wherein at least one of the solid state switches is a port bypass circuit.

5. Connection apparatus according to claim 4, wherein each solid state switch is a port bypass circuit.

10

6. Connection apparatus according to any of claims 1 to
5, wherein each network connection device is constructed and arranged to receive optical signals from an optical network and to convert the received optical signals into
15 serial electrical form for output to the respective solid state switch.

7. Connection apparatus for network testers and analysers, the connection apparatus comprising:

20 two network connection devices for connecting the apparatus to a network, each connection device being constructed and arranged to output serial electrical signals corresponding to signals received from a network to which the connection apparatus is in use connected; and,

25 two port bypass circuits, each port bypass circuit having at least three output ports, each port bypass circuit being constructed and arranged to receive serial electrical signals output by a respective one of the network connection devices and to output a corresponding
30 serial electrical signal on a first of its output ports;

each port bypass circuit being controllable such that electrical signals corresponding to signals received from a said network can selectively be output on a second of the

output ports of the port bypass circuit and received at the other port bypass circuit for return to a said network via a third of the output ports of the other port bypass circuit.

5

8. Connection apparatus according to claim 7, comprising a respective serial-to-parallel data converter for each port bypass circuit, each serial-to-parallel data converter being constructed and arranged to receive a serial
10 electrical signal corresponding to signals received from a said network that is output on the first port of the respective port bypass circuit and to convert the received serial electrical signal into parallel form.

15 9. Connection apparatus according to claim 7 or claim 8, wherein each port bypass circuit is constructed and arranged to retime electrical signals received from the other or another of the port bypass circuits prior to returning said electrical signals to a said network.

20

10. A network tester comprising connection apparatus according to any of claims 1 to 9 so that the network tester can selectively be operated in in-line or end station mode when connected to a network.

25

11. A network analyser comprising connection apparatus according to any of claims 1 to 9 so that the network analyser can selectively be operated in in-line or end station mode when connected to a network.

30

12. A method of operating connection apparatus for a network tester or analyser, the connection apparatus comprising at least two network connection devices for

connecting the apparatus to a network, each connection device being constructed and arranged to output serial electrical signals corresponding to signals received from the network; and, at least two solid state switches, each
5 solid state switch being constructed and arranged to receive serial electrical signals output by a respective one of the network connection devices and to output a corresponding serial electrical signal; the method comprising:

10 selectively controlling each solid state switch such that electrical signals corresponding to signals received from the network are output by the solid state switch and received at the other or another of the solid state
15 switches for return to the network by said other or another of the solid state switches whereby the apparatus operates in in-line mode, or such that electrical signals corresponding to signals received from the network and output by each solid state switch are not received at the other or another of the solid state switches whereby the
20 apparatus operates in end station mode.

13. A method according to claim 12, comprising converting from serial to parallel form a serial electrical signal corresponding to signals received from the network that is
25 output by one of the solid state switches.

14. A method according to claim 12 or claim 13, comprising retiming electrical signals received from the other or another of the solid state switches prior to returning said
30 electrical signals to the network.

15. A method according to any of claims 12 to 14, wherein at least one of the solid state switches is a port bypass circuit.

5 16. A method according to claim 15, wherein each solid state switch is a port bypass circuit.

17. A method according to any of claims 12 to 16,
comprising receiving optical signals from an optical
10 network at each network connection device and converting the received optical signals into serial electrical form for output to the respective solid state switch.

18. A method of operating connection apparatus for network
15 testers and analysers, the connection apparatus comprising: two network connection devices for connecting the apparatus to a network, each connection device being constructed and arranged to output serial electrical signals corresponding to signals received from a network to which the connection
20 apparatus is in use connected; and, two port bypass circuits, each port bypass circuit having at least three output ports, each port bypass circuit being constructed and arranged to receive serial electrical signals output by a respective one of the network connection devices and to
25 output a corresponding serial electrical signal on a first of its output ports; the method comprising:

selectively controlling each port bypass circuit such that electrical signals corresponding to signals received from the network are output on a second of the output ports
30 of the port bypass circuit and received at the other port bypass circuit for return to the network via a third of the output ports of the other port bypass circuit whereby the apparatus operates in in-line mode, or such that electrical

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signals corresponding to signals received from the network and output by each port bypass circuit are not received at the other port bypass circuit whereby the apparatus operates in end station mode.

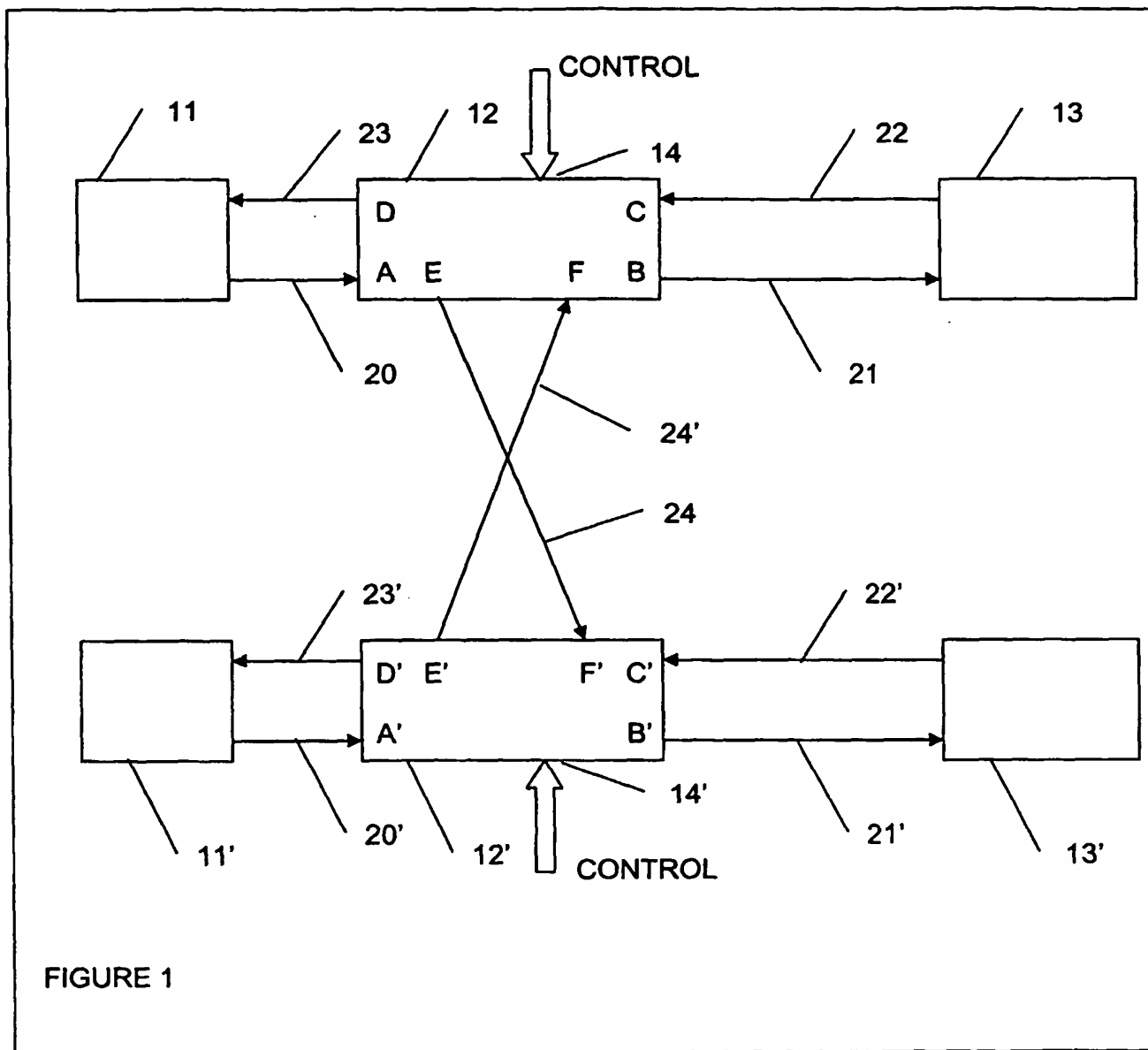
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19. A method according to claim 18, comprising converting from serial to parallel form a serial electrical signal corresponding to signals received from the network that is output on the first port of one of the port bypass

10 circuits.

20. A method according to claim 18 or claim 19, comprising retiming electrical signals received from the other or another of the port bypass circuits prior to returning said

15 electrical signals to the network.



INTERNATIONAL SEARCH REPORT

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A. CLASSIFICATION OF SUBJECT MATTER
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According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC 7 H04L

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
Y	US 2002/046276 A1 (COFFEY AEDAN DIARMUID CAILEAN ET AL) 18 April 2002 (2002-04-18) figures 1,4,6 paragraph [0002] paragraph [0029] paragraph [0080] - paragraph [0081] paragraph [0104]	1-20
Y	US 5 991 891 A (HAHN DENNIS J ET AL) 23 November 1999 (1999-11-23) figures 2,3,6 column 2, line 59 - line 67 column 5, line 43 - line 49 --- -/--	1-20

☒ Further documents are listed in the continuation of box C.

☒ Patent family members are listed in annex.

* Special categories of cited documents :

- "A" document defining the general state of the art which is not considered to be of particular relevance
- "E" earlier document but published on or after the international filing date
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INTERNATIONAL SEARCH REPORT

In International Application No
PCT/GB 03/04246

C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT		
Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	US 2002/044561 A1 (COFFEY AEDAN DIARMUID CAILEAN) 18 April 2002 (2002-04-18) figures 1,4,6,7 paragraph [0017] paragraph [0033] paragraph [0083] - paragraph [0084] paragraph [0108] ---	1-20
A	EP 0 566 139 A (SUMITOMO ELECTRIC INDUSTRIES) 20 October 1993 (1993-10-20) figures 1-3 column 2, line 22 - line 39 column 4, line 10 - line 57 column 7, line 2 - line 24 ---	1-20
A	WO 00 30293 A (VIXEL CORP) 25 May 2000 (2000-05-25) figures 13,16,19,20 page 6, line 34 - line 38 page 8, line 20 - line 28 page 13, line 23 - line 38 page 19, line 18 - line 40 ---	1-20
A	EP 0 559 209 A (PITNEY BOWES) 8 September 1993 (1993-09-08) figure 4 column 4, line 37 - column 5, line 19 column 7, line 33 - line 48 ---	1
A	US 5 812 528 A (VANDERVORT COLE S) 22 September 1998 (1998-09-22) figures 3A-3C,5 column 5, line 47 - line 53 column 10, line 40 - line 58 column 11, line 9 - line 12 ---	6,10-12, 18
A	US 5 577 023 A (MARUM JOHN ET AL) 19 November 1996 (1996-11-19) figures 17,18 column 32, line 60 - column 33, line 53 column 35, line 36 - line 64 ---	1,3,12, 14
A	US 5 627 819 A (DEV ROGER ET AL) 6 May 1997 (1997-05-06) figure 1 column 5, line 11 - column 6, line 3 ---	1,12
A	US 2002/097460 A1 (SAKANO SHINJI ET AL) 25 July 2002 (2002-07-25) figure 5 paragraph [0009] - paragraph [0010] paragraph [0021] - paragraph [0023] paragraph [0043] -----	6,17

INTERNATIONAL SEARCH REPORT

Information on patent family members

International Application No

PCT/GB 03/04246

Patent document cited in search report		Publication date		Patent family member(s)	Publication date
US 2002046276	A1	18-04-2002	IE	20010396 A2	06-02-2002
US 5991891	A	23-11-1999	AU WO	5527498 A 9828881 A1	17-07-1998 02-07-1998
US 2002044561	A1	18-04-2002	IE US	20010395 A2 2002044562 A1	06-02-2002 18-04-2002
EP 0566139	A	20-10-1993	JP JP DE DE EP KR US	3149518 B2 5300200 A 69317006 D1 69317006 T2 0566139 A1 260247 B1 5388091 A	26-03-2001 12-11-1993 26-03-1998 02-07-1998 20-10-1993 01-07-2000 07-02-1995
WO 0030293	A	25-05-2000	AU WO	1722400 A 0030293 A2	05-06-2000 25-05-2000
EP 0559209	A	08-09-1993	US CA DE DE EP	5400345 A 2091087 A1 69332051 D1 69332051 T2 0559209 A2	21-03-1995 07-09-1993 01-08-2002 19-12-2002 08-09-1993
US 5812528	A	22-09-1998	AU CA EP WO	1271997 A 2237986 A1 0861534 A1 9719535 A1	11-06-1997 29-05-1997 02-09-1998 29-05-1997
US 5577023	A	19-11-1996	US AU AU AU AU CA EP WO	5414708 A 692741 B2 4691197 A 687026 B2 5734794 A 2148820 A1 0672319 A1 9413072 A1	09-05-1995 11-06-1998 12-02-1998 19-02-1998 22-06-1994 09-06-1994 20-09-1995 09-06-1994
US 5627819	A	06-05-1997	AT AU AU DE DE EP JP WO US	190787 T 686048 B2 4856796 A 69607142 D1 69607142 T2 0803157 A2 10512413 T 9621982 A2 5754532 A	15-04-2000 29-01-1998 31-07-1996 20-04-2000 28-09-2000 29-10-1997 24-11-1998 18-07-1996 19-05-1998
US 2002097460	A1	25-07-2002	JP	2002223197 A	09-08-2002